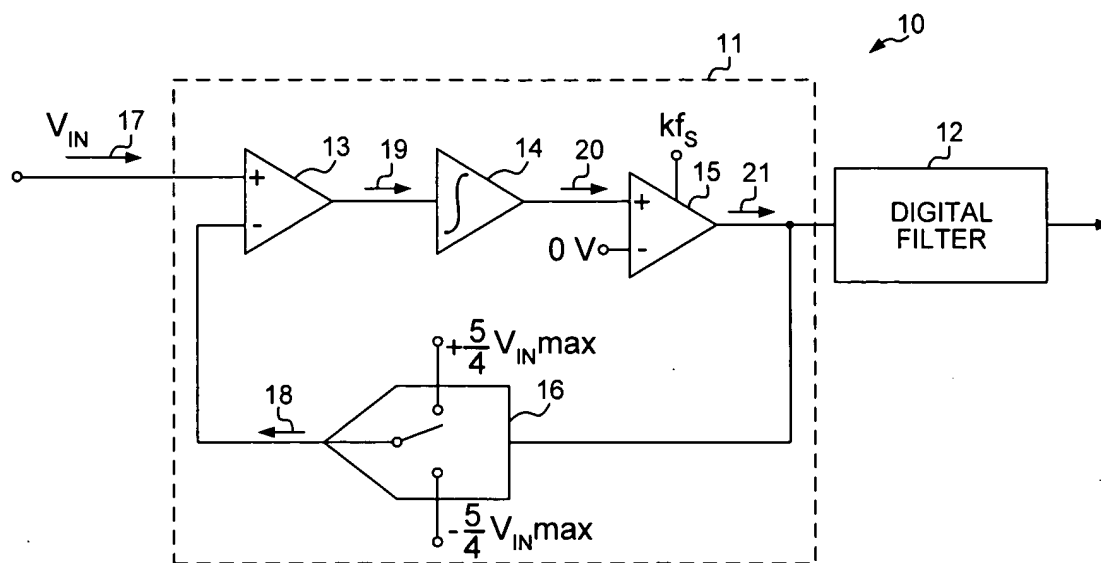
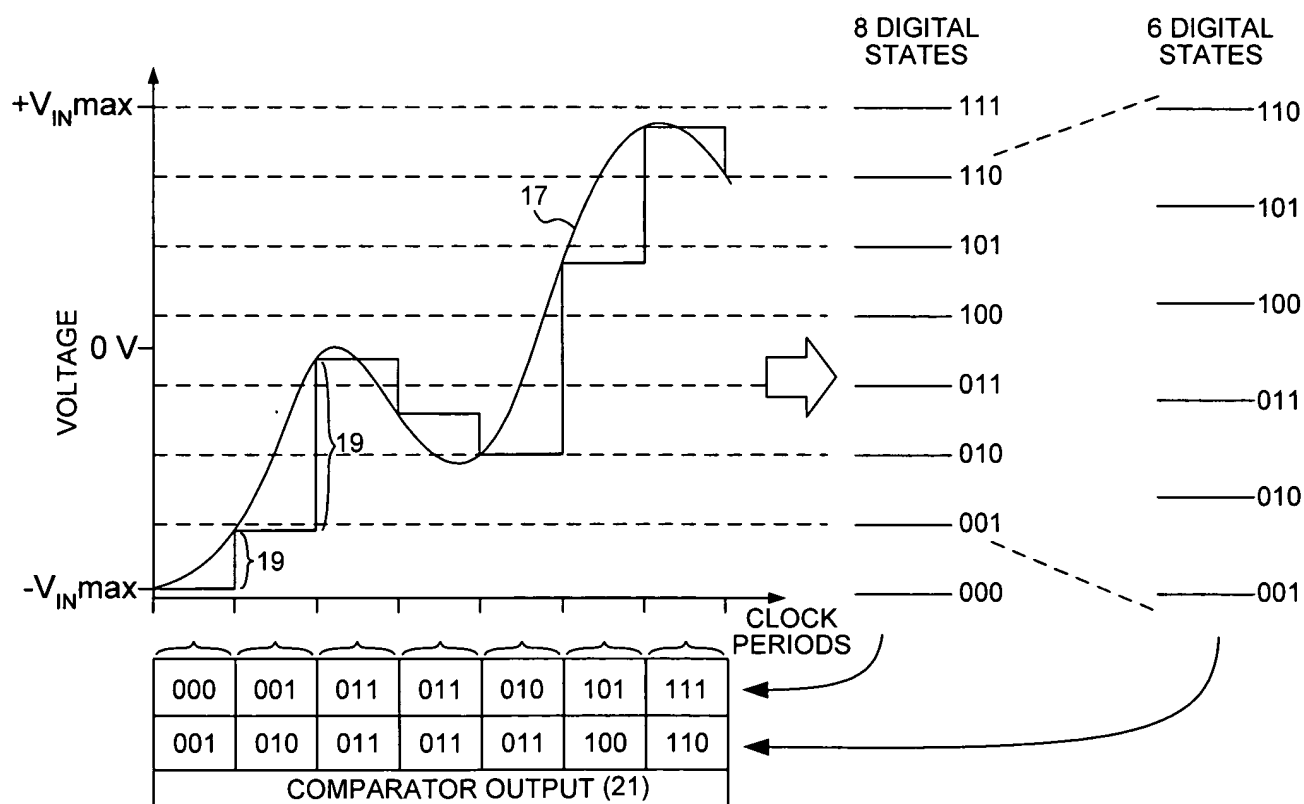


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**FIG. 1**  
(PRIOR ART)



**FIG. 2**  
(PRIOR ART)



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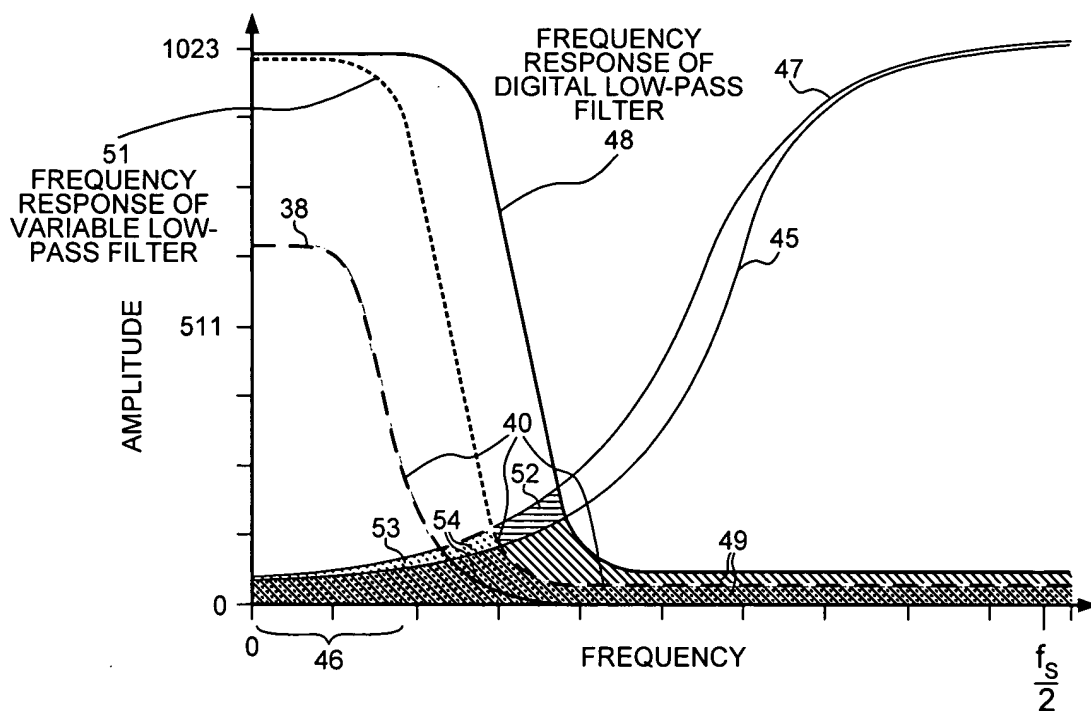


FIG. 5

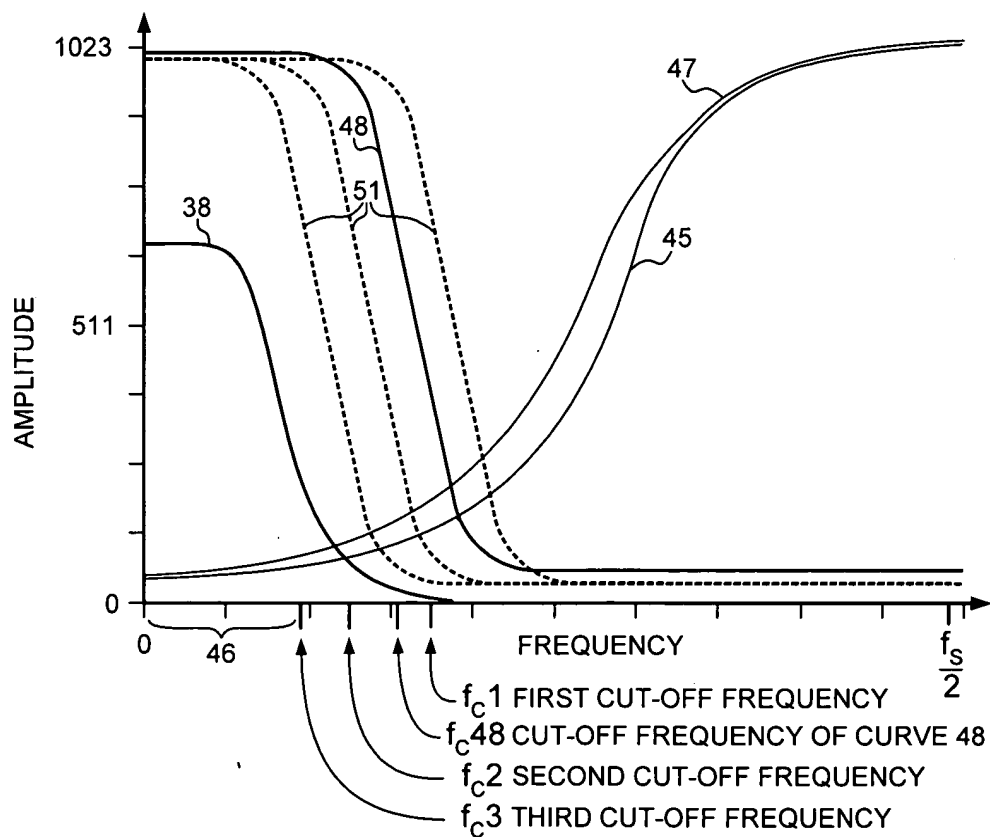


FIG. 6

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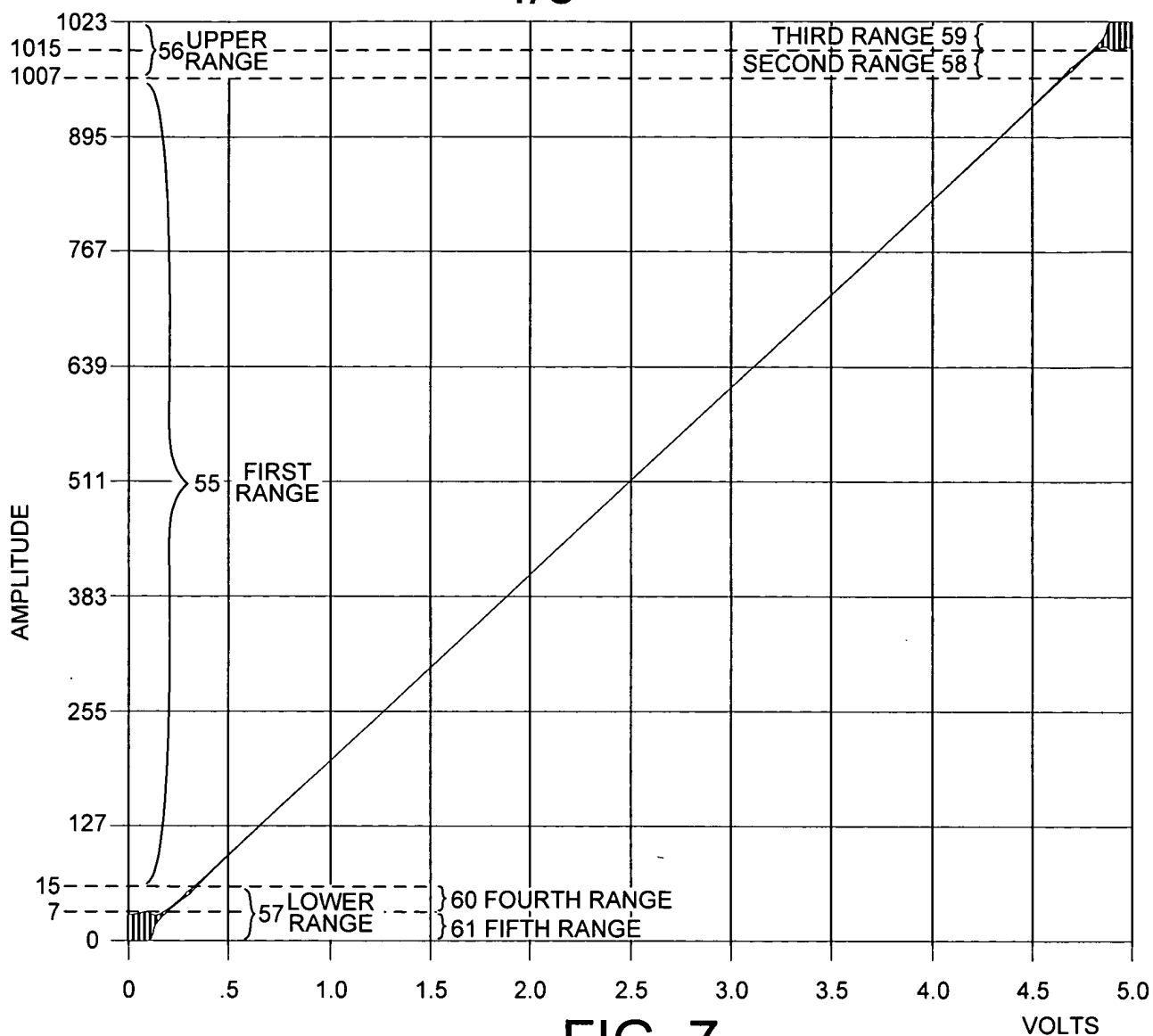
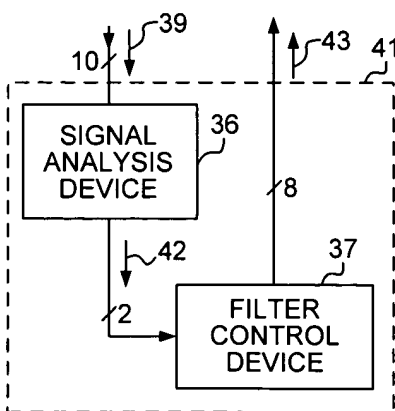


FIG. 7



NONLINEAR CONTROL BLOCK

FIG. 9

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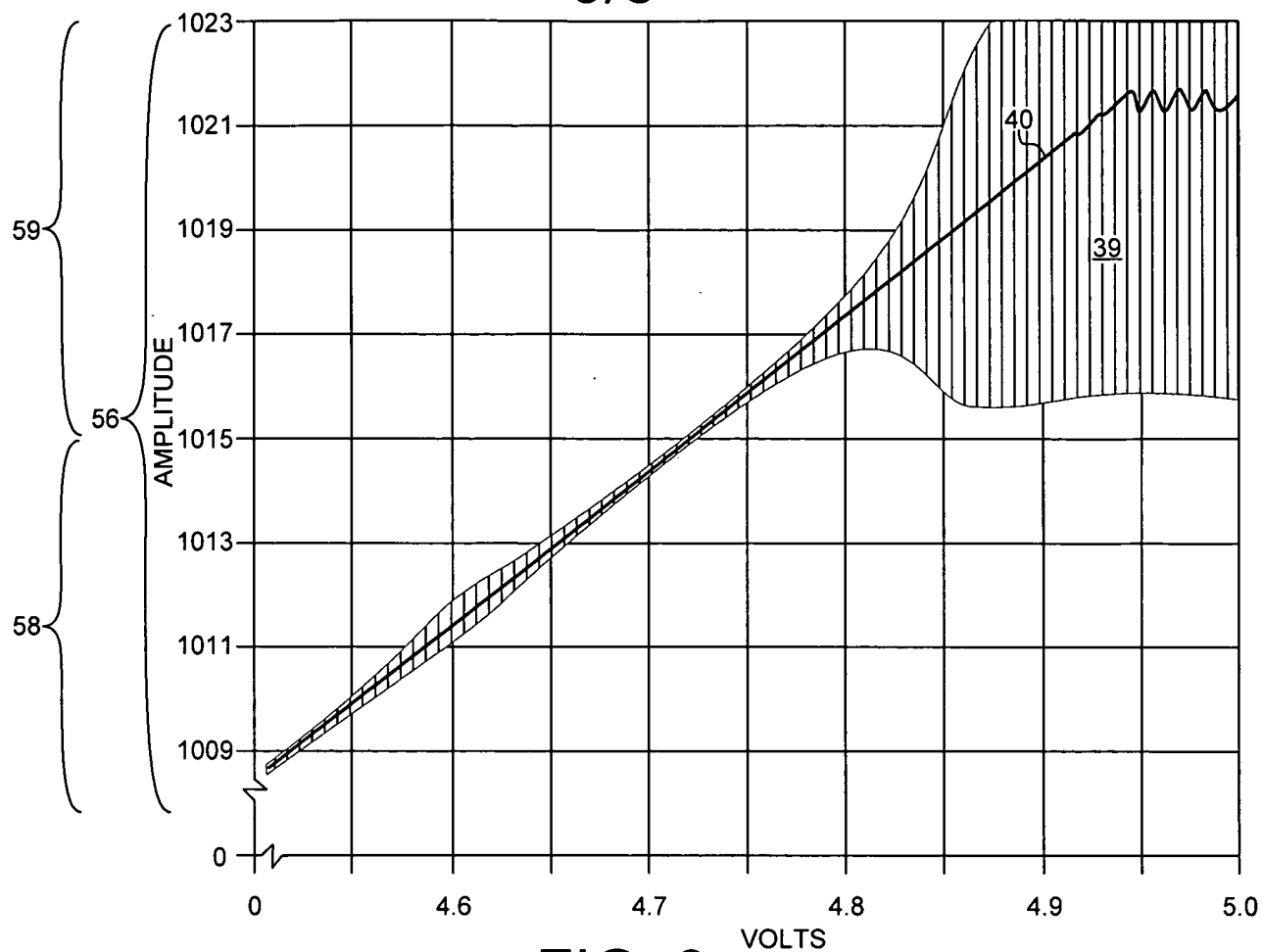
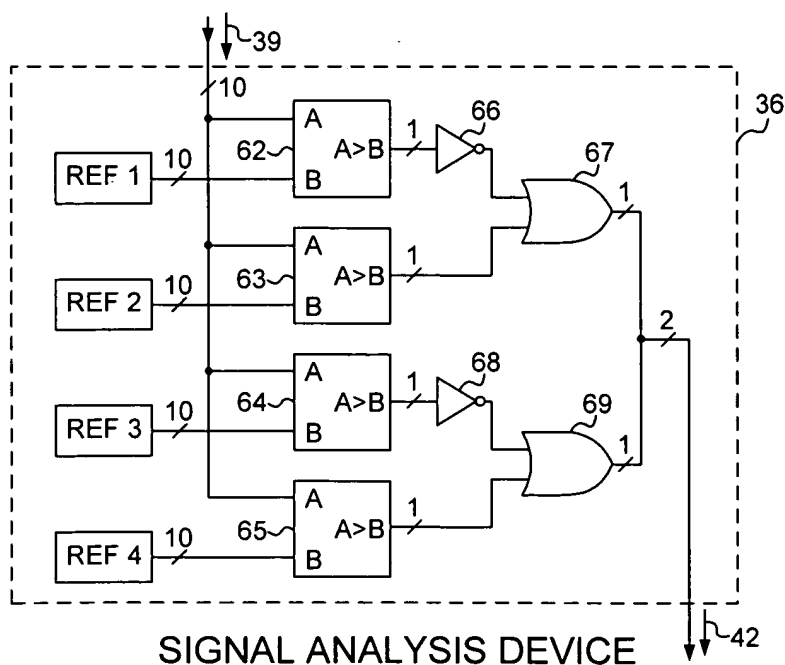


FIG. 8



SIGNAL ANALYSIS DEVICE

FIG. 10



FIG. 11



FIG. 12

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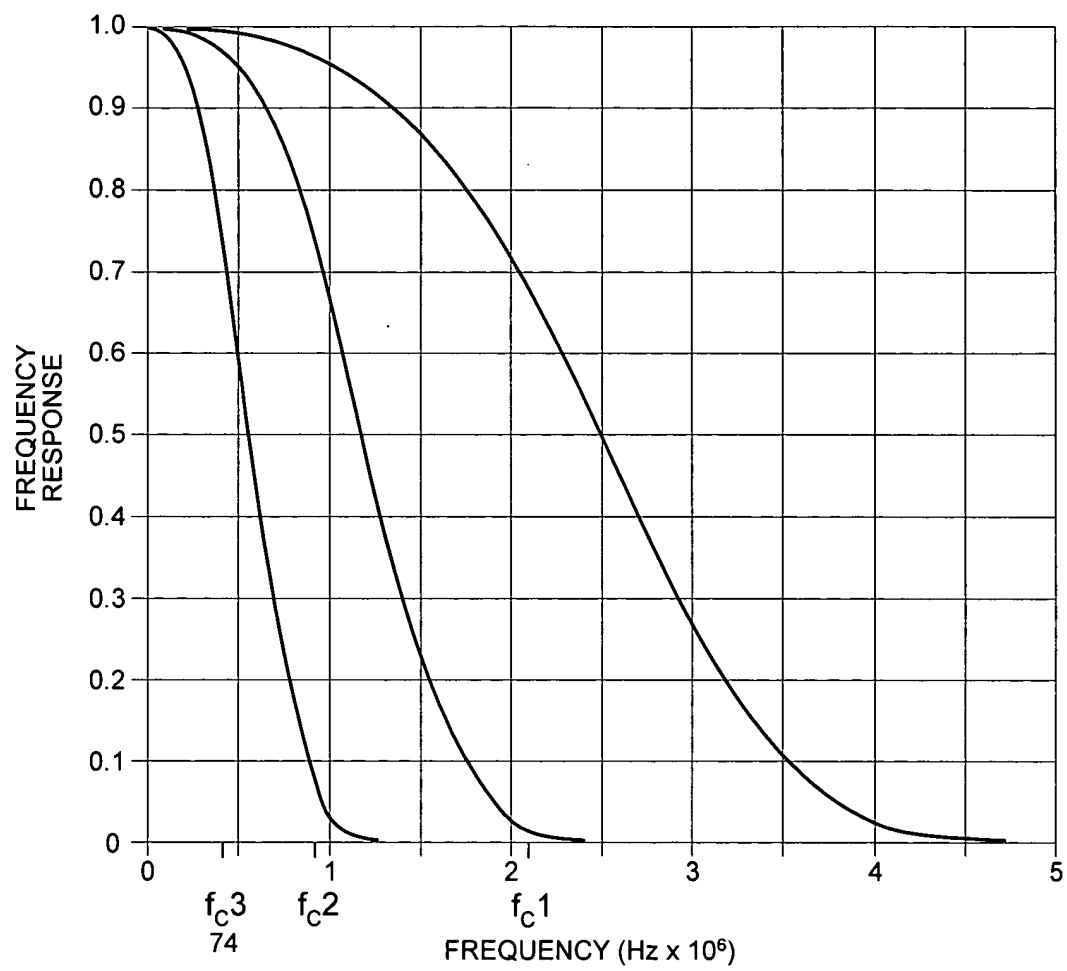


FIG. 13

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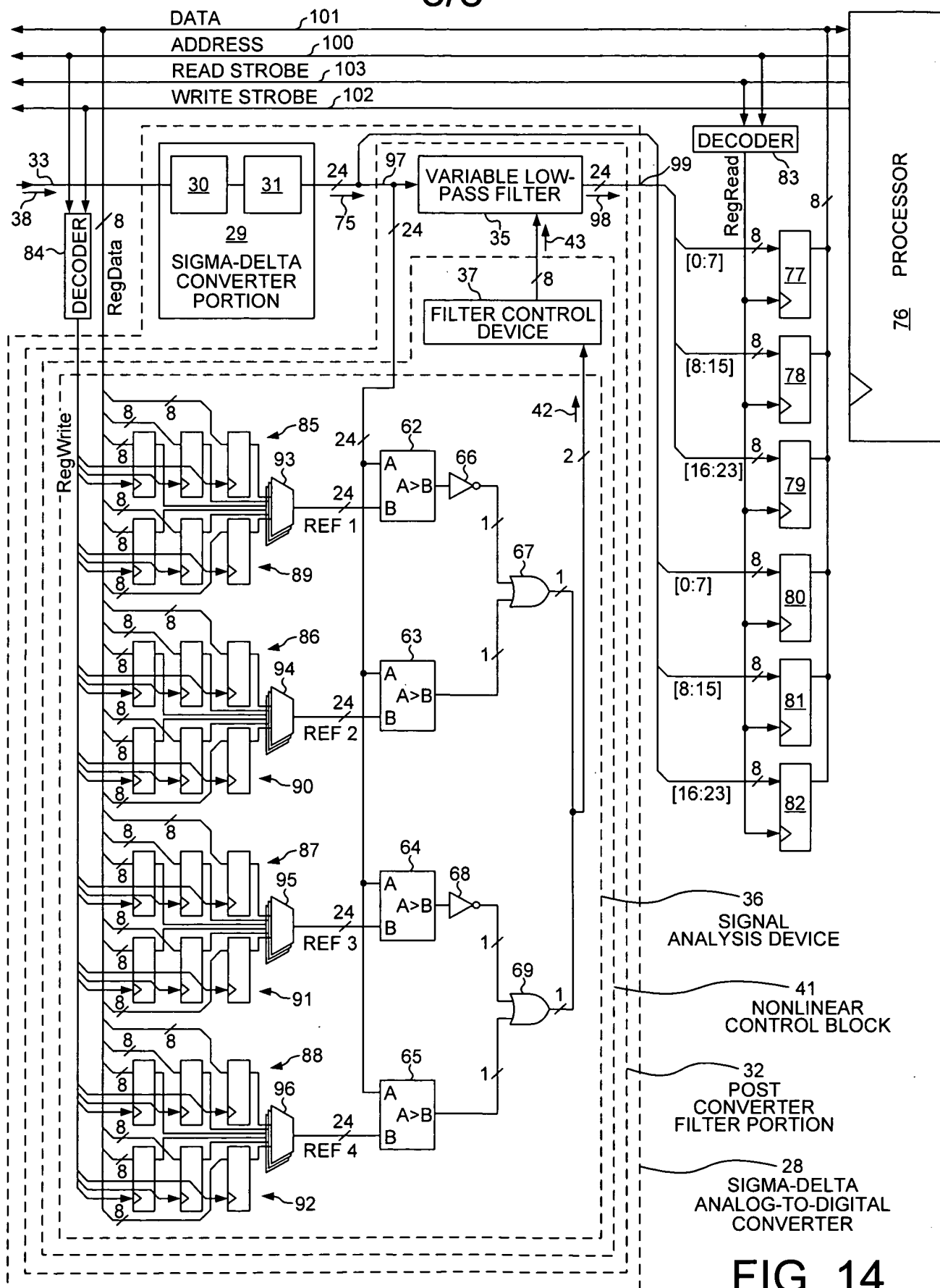


FIG. 14